Moore Microprocessor Portfolio (MMP) Inventor Files Lawsuit against TPL Group

SAN JOSE, CA -- Sept. 28, 2010 --
Silicon Valley technologist and legendary inventor Charles H. Moore today announced that he has filed suit in Superior Court of Santa Clara, CA (. http://www.sccsuperiorcourt.org/civil/default.htm. Case # 110CV183613) against the TPL Group and Alliacense LLC, patent licensing firms based in Cupertino, CA, for fraud, breach of fiduciary duty, and breach of contract. Moore is also requesting preliminary injunctive relief against TPL and Alliacense. Moore has developed scores of patents over the past 40 years including those in the eponymous MMP Portfolio . He is also known worldwide as the inventor of the FORTH programming system.

Moore has also filed suit individually against the officers of TPL and Alliacense, namely: Daniel E. Leckrone, TPL Group Chairman; Daniel M. Leckrone, Alliacense President; and Michael Davis, Alliacense Senior Vice President of Licensing.

In April of this year, Patriot Scientific (PTSC), part owner of the MMP Portfolio and business partner of the TPL Group filed suit in San Diego against the TPL Group alleging a contractual breach on a $1 million note receivable. In a separate court action against TPL in Santa Clara County, Court documents indicate PTSC has sought and won a temporary restraining order against TPL. TPL later stipulated that the TRO would become a preliminary injunction.

A motion for a Preliminary injunction against TPL is scheduled for November 2, 2010 in Santa Clara County Superior Court.

About the MMP Portfolio

The MMP Portfolio includes seven U.S. patents as well as their European and Japanese counterparts. It is widely recognized that the MMP Portfolio protects fundamental technology used in microprocessors, microcontrollers, digital signal processors (DSPs), embedded processors and system-on-chip (SoC) devices. The Portfolio has upward of seventy licensees, including Intel, AMD, HP, Sony, Panasonic, Philips, Sharp, Robert Bosch,Toshiba and Fujitsu.

About Charles H. Moore

Charles H. "Chuck" Moore is known for inventing the FORTH software programming language and for his work in the 1980's on stack based microprocessors. During the 1990's, Moore used his own CAD software to design several custom VLSI chips, including the F21 processor with a network interface. More recently, he invented ColorForth and ported his VLSI design tools to it. In 2009, Moore started a new chip design firm, GreenArrays, Inc, where he sits as Chairman and Chief Technical Officer.

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